



PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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W. L. L. L. L.  
3/11/04  
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Appl. No. : 09/441,119	Confirmation No.: 3874
Applicant : Oliver L. Richards, et al.	
Filed : November 17, 1999	
T.C./A.U. : 2611	
Examiner : Usha Raman	
Docket No. : ALLEG-017PUS	
Customer No. : 022494	

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1 March 2004  
Date of Signature  
and Mail Deposit

By: Judith C. Crowley  
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MS Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Technology Center 2600

Dear Sir:

**AFFADAVIT UNDER 37 CFR §1.131**

1. I, Oliver L Richards, am a co-inventor of a U.S. Patent Application entitled "Low Noise Block Supply and Control Voltage Regulator" now pending before the U. S. Patent Office as Application No. 09/441,119.

2. Prior to December 1999, I, along with co-inventor Paul M. Greenland, conceived of the invention described in U. S. Patent Application No. 09/441,119, as evidenced by a schematic captioned "LNB BUCK/BOOST PROPOSAL" and attached hereto as Exhibit A.

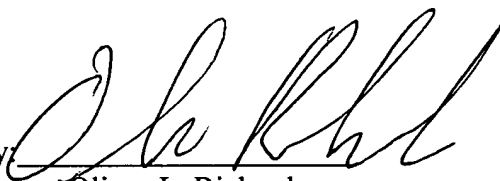


3. Following conception, I worked on developing a product incorporating the technology described in U. S. Patent Application No. 09/441,119, as evidenced by a schematic captioned "LNB BUCK REGULATOR PROPOSAL," dated 31 March 1999, and attached hereto as Exhibit B as well as a Product Preview datasheet for the 8284 Product of Allegro Microsystems, Inc. dated 19 July 1999 and attached hereto as Exhibit C.

4. The subject patent application was filed in the U.S. Patent Office on November 17, 1999.

5. All of the statements made herein are of my own knowledge and are true, these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under § 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application and any patent issuing thereon, or any patent to which this verified statement is applied.

1-30-04  
Date of Signature

By   
Oliver L. Richards

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